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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application / Conf. No.	Unknown /
Filing Date	October 10, 2003
First Named Inventor	David Mark
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1338 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 5,914,616	06-22-99	Young et al.	
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Foreign Patent Document Country Code [3] -Number [4] -Kind Code [5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T [6]

Examiner Signature		Date Considered	

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[1] Applicant's unique citation designation number (optional). [2] See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

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OTHER -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		Xilinx, Inc.; "The Programmable Logic Data Book 2000"; published April 2000; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp 3-75 through 3-96.	
		Xilinx, Inc.; XAPP290 v1.0 Application Note; "Two Flows for Partial Reconfiguration: Module Based or Small Bit Manipulations"; published May 17, 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; 23 pages.	

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